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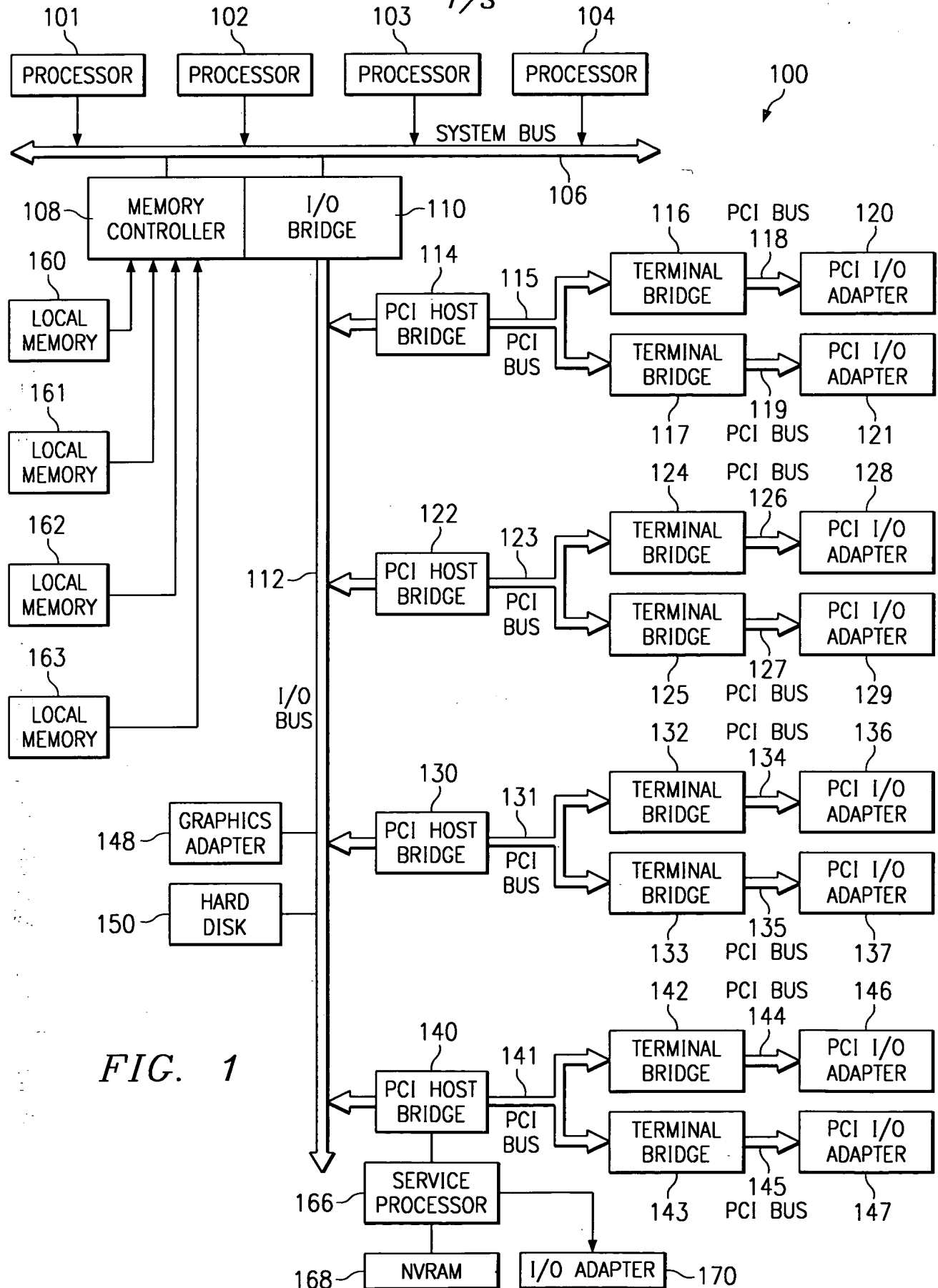
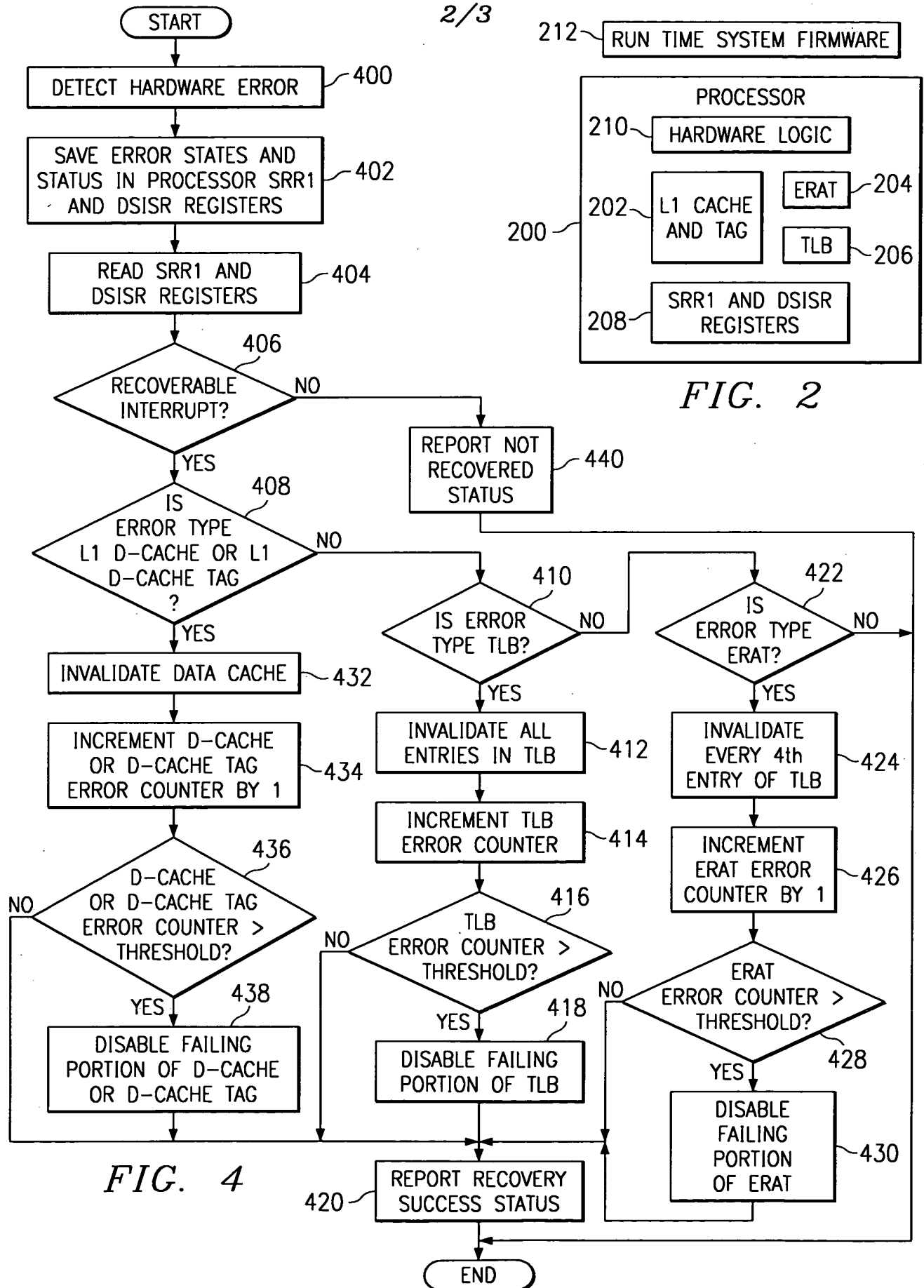


FIG. 1

212 RUN TIME SYSTEM FIRMWARE



300

FIG. 3

302	304	306	308	310
ERROR DESCRIPTION	HARDWARE INDICATION	FIRMWARE RECOVERY	LOG RETURN SEVERITY	LOG RETURN RTAS DISPOSITION
312 - LOST HW STATES	SRR1[RI] = 0	NONE	FATAL (ERROR NOT SYNCHRONOUS TO CURRENT CONTEXT)	NOT RECOVERED
318 - D-CACHE PARITY - D-CACHE TAG PARITY UNDER THRESHOLD	SRR1[RI] = 1 AND SRR1[43] = 1 AND DSISR[18/19] = 1	INVALIDATE D-CACHE	WARNING	FULLY RECOVERED
320 - D-CACHE PARITY - D-CACHE TAG PARITY OVER THRESHOLD	SRR1[RI] = 1 AND SRR1[43] = 1 AND DSISR[18/19] = 1	- INVALIDATE D-CACHE - DISABLE FAILING PORTION OF D-CACHE	WARNING	FULLY RECOVERED
322 - L/S TLB PARITY - L/S D-ERAT PARITY UNDER THRESHOLD	SRR1[RI] = 1 AND SRR1[43] = 1 AND DSISR[20/21] = 1	INVALIDATE TLB ALL	WARNING	FULLY RECOVERED
314 - L/S TLB PARITY - L/S D-ERAT PARITY OVER THRESHOLD	SRR1[RI] = 1 AND SRR1[43] = 1 AND DSISR[20/21] = 1	INVALIDATE TLB ALL, DISABLE FAILING PORTION OF ERAT OR TLB	WARNING	FULLY RECOVERED
324 - IFETCH TLB PARITY UNDER THRESHOLD	SRR1[RI] = 1 AND SRR1[44:45] = 10	INVALIDATE TLB ALL	WARNING	FULLY RECOVERED
316 - IFETCH TLB PARITY OVER THRESHOLD	SRR1[RI] = 1 AND SRR1[44:45] = 10	INVALIDATE TLB ALL, DISABLE FAILING PORTION OF TLB	WARNING	FULLY RECOVERED